

**PHOTOELECTRIC SYNCHRONOUS ISOLATING METHOD FOR MICRO-COMPUTER SYSTEM I/O BUS**

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**Inventor:** HE HUANG (CN)  
**Applicant:** UNIV SHANGHAI JIAOTONG (CN)  
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**Abstract of CN1061861**

The invention relates to a photoelectric synchronous isolation method of I/O bus in microcomputer system in the signal transmission between microcomputer and the peripheral devices. The invention adopts photoelectric coupler to effect electric isolation of the I/O bus of the computer system and the peripheral channel bus. It includes address bus isolation, data bus isolation and control bus isolation. This I/O bus synchronous isolation designed with simple concept and less elements and circuits, effects synchronous output and input of microcomputer bus signals to raise the reliability of industrial control and monitoring by microcomputer system.

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